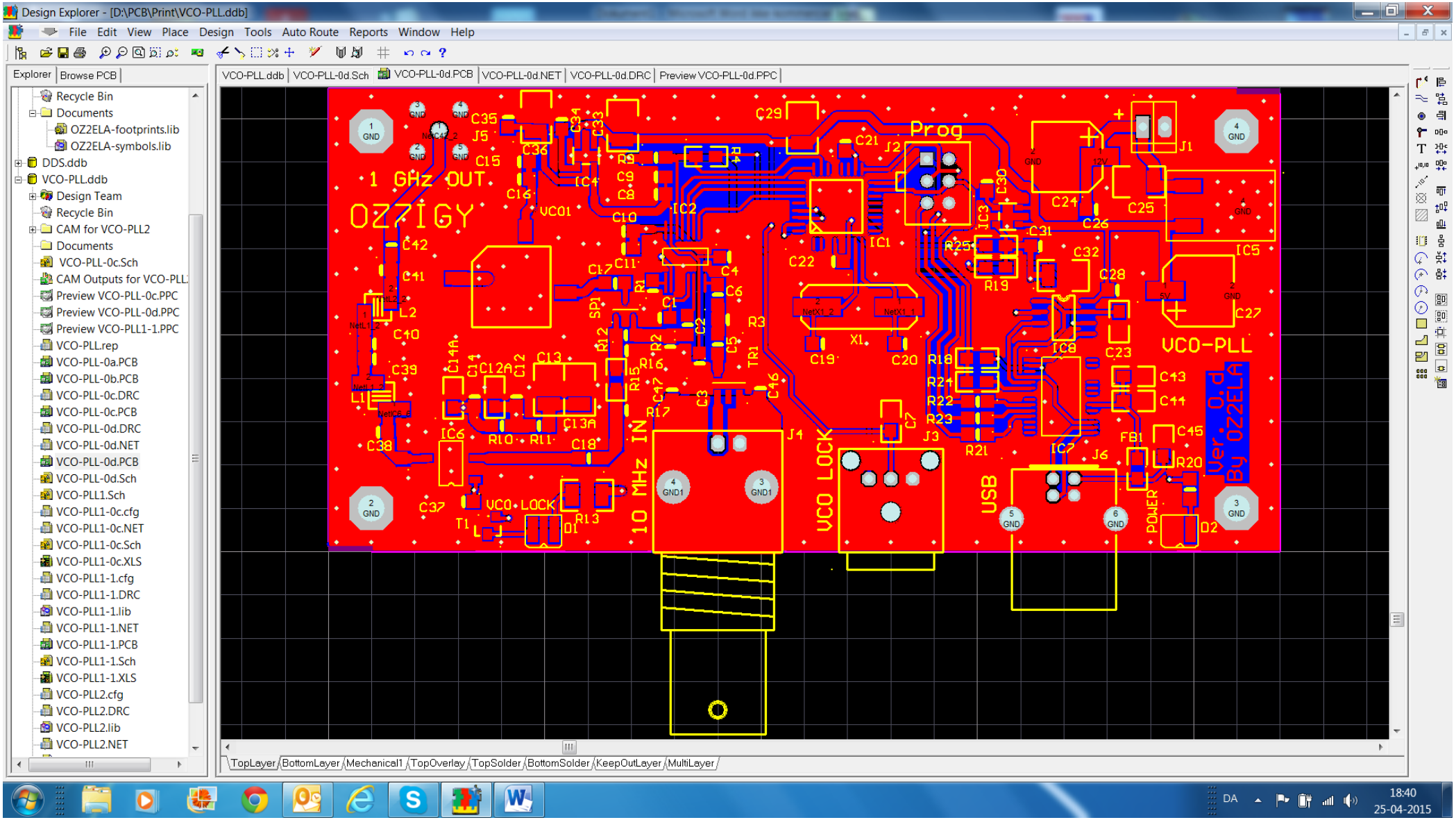
Design Explorer - [D:\PCB\Print\VCO-PLL.ddb]
File Edit View Place Design Tools Auto Route Reports Window Help
Explorer Browse PCB
Recycle Bin
Documents
OZ2ELA-footprints.lib
OZ2ELA-symbols.lib
DDS.ddb
VCO-PLL.ddb
Design Team
Recycle Bin
CAM for VCO-PLL2
Documents
VCO-PLL-0c.Sch
CAM Outputs for VCO-PLL
Preview VCO-PLL-0c.PPC
Preview VCO-PLL-0d.PPC
Preview VCO-PLL1-1.PPC
VCO-PLL.rep
VCO-PLL-0a.PCB
VCO-PLL-0b.PCB
VCO-PLL-0c.DRC
VCO-PLL-0c.PCB
VCO-PLL-0d.DRC
VCO-PLL-0d.NET
VCO-PLL-0d.PCB
VCO-PLL-0d.Sch
VCO-PLL1.Sch
VCO-PLL1-0c.cfg
VCO-PLL1-0c.NET
VCO-PLL1-0c.Sch
VCO-PLL1-0c.XLS
VCO-PLL1-1.cfg
VCO-PLL1-1.DRC
VCO-PLL1-1.lib
VCO-PLL1-1.NET
VCO-PLL1-1.PCB
VCO-PLL1-1.Sch
VCO-PLL1-1.XLS
VCO-PLL2.cfg
VCO-PLL2.DRC
VCO-PLL2.lib
VCO-PLL2.NET
VCO-PLL.ddb
VCO-PLL-0d.Sch
VCO-PLL-0d.PCB
VCO-PLL-0d.NET
VCO-PLL-0d.DRC
Preview VCO-PLL-0d.PPC
1 GHz OUT
OZ2IGY
Prog
VCO-PLL
Ver. 0
By OZ2ELA
10 MHz IN
VCO LOCK
USB
POWER
TopLayer
BottomLayer
Mechanical1
TopOverlay
TopSolder
BottomSolder
KeepOutLayer
MultiLayer
DA
18:40
25-04-2015